

**REQUEST  
FOR  
CONTINUED EXAMINATION (RCE)  
TRANSMITTAL**

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000, provides for continued examination of an utility or plant application filed on or after June 8, 1995.

See The American Inventors Protection Act of 1999 (AIPA).

<i>Application Number</i>	10/810,035
<i>Filing Date</i>	March 26, 2004
<i>First Named Inventor</i>	Albert S. Weiner
<i>Confirmation Number</i>	8458
<i>Group Art Unit</i>	2818
<i>Examiner Name</i>	Thinh Nguyen
<i>Attorney Docket Number</i>	2800.450US1
<i>Customer No.</i>	76287

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application entitled

Non-volatile transistor memory array incorporating read-only elements with single mask set

1. Submission required under 37 C.F.R. § 1.114:

Information Disclosure Statement (2 pages), Form 1449 (1 page), and copy of cited document (1).

2. Fees

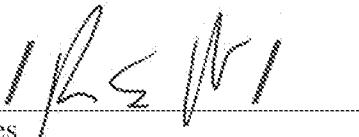
Authorization to charge deposit account 19-0743 in the amount of \$810.00 to pay the RCE filing fee required under 37 C.F.R. § 1.17(e).

The Commissioner is hereby authorized to charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.

By: \_\_\_\_\_

Robert E. Mates  
Reg. No. 35,271



CERTIFICATE UNDER 37 C.F.R. 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 25th day of November, 2009.

Nicole Jack

Name \_\_\_\_\_

Signature \_\_\_\_\_

